The Convergence of HPC and Big Data

Pete Beckman

Senior Scientist, Argonne National Laboratory Co-Director, Northwestern / Argonne Institute for Science and Engineering (NAISE) Senior Fellow, University of Chicago Computation Institute

Argonne National Laboratory

- \$675M /yr budget
- 3,200 employees
- 1,450 scientists/eng
- 750 Ph.D.s

Argonne's Next Big Supercomputer: Aurora



Europe-USA-Asia

International series of Workshops on Extreme Scale Scientific Computing

Following the International Exascale Software Initiative (IESP 2008-2012 → Big Data and Extreme Computing workshops (BDEC) <u>http://www.exascale.org/bdec/</u>

Overarching goal:

- 1. Create an international collaborative process focused on the co-design of software infrastructure for extreme scale science, addressing the challenges of both extreme scale computing and big data, and supporting a broad spectrum of major research domains,
- 2. Describe funding structures and strategies of public bodies with Exascale R&D goals worldwide
- 3. Establishing and maintaining a global network of expertise and funding bodies in the area of Exascale computing
- 1 BDEC Workshop, Charleston, SC, USA, April 29-May1, 2013
- 2 BDEC Workshop, Fukuoka, Japan, February 26-28, 2014
- 3 BDEC Workshop, Barcelona, Spain, January 28-30, 2015
- 4 BDEC Workshop, Frankfurt, Germany, June 15-17, 2016



BIG DATA AND EXTREME-SCALE COMPUTING





BDEC = Convergence of Big Data & HPC

EXDCI WP6 International Liaisons

Mark Asch Université de Picardie, Amiens

ORAP #38- October 18th, 2

The EXDCI project has received funding from the European Union's Horizon 2020 research and innovation programmed under grant agree

What is EXDCI?

- EXDCI is a 30-month project starting from September 2015 with
 - a budget of € 2.5 million
 - with 173,5 PMs
 - funded in call FET-HPC 2014.
- EXDCI's objective is to support the coordination of the development and implementation of a common strategy for the European HPC Ecosystem.

EXDCI WP6 Task 6.3: EXDCI in Europe and Worldwide (aka BDEC)

Courtesy: Mark Asch



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BDEC @ Europe











Why Converge? Independent paths: More Cost, Less Science,

- \$ multiple hardware software infrastructures
- \$ developing software for two communities
- \$ learning two computing models
- \$ smaller discovery community, fewer ideas
- Less science





An Exascale Operating System and Runtime Software Research & Development Project

Developing vendor neutral, open-source OS/R software

ANL: Pete Beckman (PI), Marc Snir (Chief Scientist), Pavan Balaji, Rinku Gupta, Kamil Iskra, Franck Cappello, Rajeev Thakur, Kazutomo Yoshii
LLNL: Maya Gokhale, Edgar Leon, Barry Rountree, Martin Schulz, Brian Van Essen
PNNL: Sriram Krishnamoorthy, Roberto Gioiosa
UC: Henry Hoffmann
UIUC: Laxmikant Kale, Eric Bohm, Ramprasad Venkataraman
UO: Allen Malony, Sameer Shende, Kevin Huck
UTK: Jack Dongarra, George Bosilca, Thomas Herault

See <u>http://www.argo-osr.org/</u> for more information

What OS/R Gaps *Must* We Address?

• Extreme in-node parallelism

- Poor mechanisms for precise resource management (cores, power, memory, network)
- Legacy threads/tasks implementations perform poorly at scale

• Dynamic variability of platform; Power is constrained

- Poor runtime mechanisms for managing dynamic overclocking, provisioning power, adjusting workloads
- No mechanisms for managing power dynamically, globally, and in cooperation with user-level runtime layers
- Hierarchical memory
 - Poor interfaces / strategies for managing deepening memory
- New modes for HPC
 - No portable interfaces for easily building workflows, in-situ analysis, coupled physics, advanced I/O, application resilience

| 10/28/16 | Argo OSR | Pete Beckman | 15 | 15 |
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Argo Explorations to Address Exascale Gaps



Elastic intranode containers with resource knobs



Hierarchy of *Enclaves* connected via a *Backplane*



Lightweight thread/tasks designed for containers, messaging, and memory hierarchy



Adaptive, learning, integrated control system



Understanding Cities







Supported by collaborating institutions and the U.S. National Science Foundation. Industry In-Kind partners: AT&T, Cisco, Intel, Microsoft, Motorola Solutions, Schneider Electric, Zebra

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Waggle: An Open Platform for Intelligent Sensors

Exploiting Disruptive Technology, Edge Computing, Resilient Design

Machine Learning

Computer Vision



Novel Sensors Nano / MEMS



Low Power CPUs GPU / Smartphones





Powerful, Resilient & Hackable

Multiple boot media (µSD / eMMC)













Waggle / AoT Robust Testing



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Edge Computing: Analysis and Feature Recognition

Preserving Privacy...

- Parallel Computing
- Open Platform
- Deep Learning



"Tell me about the NPC 2016 conference"

tap to edit

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Let me think about that...

Waggle Machine Learning & Edge Computing



https://waggle-sensor.slack.com/files/noaholsman/F243LQL66/output.jpg

- We are exploring Caffe & OpenCV
 - Convolutional Neural Networks
- Training will be done on systems at Argonne
- Classification on Waggle





12h

















EXCLUSIVE

Chicago puts up sensors to track city's vitals

Fitbit-like tech to provide block-by-block environmental data and more

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CHICAGO The Windy City has begun installing what sounds a whole lot like a Fitbit that can measure the vitals of a bustling destrian traffic. metropolis.

several corporations, last week Plans are in the works to repliinstalled the first two of 500 cate the project in the coming modular sensor boxes. The de- years in more than a dozen other vices will eventually allow the city and public to instantly get blockby-block data on air quality, noise levels, as well as vehicular and pe-

The project - dubbed the Ar-Chicago, which partnered on ray of Things and described by

gonne National Laboratory and of-its-kind effort in the nation. cities, including Atlanta, Chattanooga, and Seattle. The Chicago project was funded with the help of a \$3.1 million National Science Foundation grant.

> "Five years out, if we're successful, this data and the applica-



Nick Stodoev installs a modular sensor box that is part of Chicago's Array of Things project.

Waggle: A Platform for Research

- Open Source / Open Platform
 - Reusable, extensible software communities

• Machine Learning: Computer Vision

- Data must be reduced in-situ
- Novel Sensors: Nano / MEMS / µfluidics
 - Explosion of nano/MEMS & imaging tech
- Low-Power CPUs: GPU / Smartphones
 - Powerful, low-power, smartphone CPUs

Opportunity: Big Data + Predictive Models

Smart Sensors + Supercomputers/Cloud Computing = predictions and analysis

Why HPC Geeks Should Care

- New sensors are *programmable* parallel computers
 - Multicore + GPUs & OpenCL or OpenMP
 - New algorithms for in-situ data analysis, feature detection, compression, deep learning
 - Need new progmod for "stackable" in-situ analysis (for sensors and HPC)
 - Need advanced OS/R resilience, cybersecurity, networking, over-the-air programming
- 1000s of nodes make a distributed computing "instrument"
 - New streaming programming model needed
 - New techniques for machine learning for scientific data required
 - Both for within a "node" and collectively across time series
- How will HPC streaming analytics and simulation be connected to live data?
 - Can we trigger HPC simulations after first approximations? (weather, energy, transportation)
 - Unstructured database with provenance and metadata for QA/collaboration
- Use novel HPC hardware to solve power issue?
 - Can we use neuromorphic or FPGAs to reduce power for in-situ analysis & compression?
- We are trading precision & cost for greater spatial resolution: What is possible?



RISC version of Convergence Story:

Start by enabling... remove roadblocks (then everyone's wish list follows)

- Applications (Science Drivers): Software needs & workflow patterns
- Operations
 - Support real-time and streaming from fast networks
 - Support node sharing, long-lived services, storage requests for years...
- Architecture
 - Mothball current parallel file systems, replace with persistent storage services (databases, KV, etc.)
 - Accelerate move of storage into compute infrastructure
- Software
 - Linux software development environment.
 - Native support for low-level infrastructure: Docker, VMs, Mesos, etc.
 - New focus on QoS; Software Defined Storage, on-demand services, etc.

Pathways to Convergence: outline of the report (1) Pathways to Convergence: outline of the report (2 Introduction Pathways to Convergence: What are the Alternatives? A Common Context for Planning Cyberinfrastructure Convergence The classical paradigm of a common infrastructure: The A More Analytical Model of Scientific Inquiry hourglass architecture. Confronting the General Problem of Data Logistics BDEC target goal onto the BD stack; use HPC technology Alternative 1: Streaming underneath Alternative 2: Content Delivery Networks (Processing In-Continue in the Bifurcated world, but do containerization to transit) achieve some commonality Alternative 3: Computing at the Edge (at the Source) An Architecture for the New Stack Other material relevant to this section: cture Section Software Infrastructure Pathways to Convergence: outline of the report (3) ing of convergence: Building a new Stakeholders in Sol What is to be gained fi Trends in the Hardware Platform Substrate ne new "narrow waist"? quate HPC and security capabilities Applications in the Scientific Research Process ("Applications") Taxonomy of Application/workflow patterns and templates Science at the boundary of observation and simulation **BOF SC2016** Numerical laboratories Multimodal sensing of the same reference object Layers and Components in the Scientific Software Stack Math Libraries, Software Ecosystems for app development Operations and Systems Management Radically improved resource management **Courtesy: Mark Asch** QoS and Performance tools

Conclusions and Recommendations



Questions?